

L Number	Hits	Search Text	DB	Time stamp
10	121554	(NMOS and PMOS) or CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:13
11	39548	"exclusive OR"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:14
12	4172	"exclusive NOR"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:14
13	41815	"exclusive OR" or "exclusive NOR"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:14
15	4644	full adj adder	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:16
16	376	((NMOS and PMOS) or CMOS) and ("exclusive OR" or "exclusive NOR") and (full adj adder)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:16
17	343927	inverter inverting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:16
18	317	((((NMOS and PMOS) or CMOS) and ("exclusive OR" or "exclusive NOR") and (full adj adder)) and (inverter inverting)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:17
19	51219	sum SAME (carry out)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:17
20	243	(((((NMOS and PMOS) or CMOS) and ("exclusive OR" or "exclusive NOR") and (full adj adder)) and (inverter inverting)) and (sum SAME (carry out))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:18

22	356023	multiplexer multiplexor selector (transmission adj gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:19
23	192	(((NMOS and PMOS) or CMOS) and ("exclusive OR" or "exclusive NOR") and (full adj adder)) and (inverter inverting)) and (sum SAME (carry out))) and (multiplexer multiplexor (transmission adj gate))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/25 16:19